In The Abstract

Please insert the following abstract which is also attached on a separate sheet hereto:

--- **ABSTRACT OF THE DISCLOSURE**

A noise canceling circuit, comprising: a first source terminal; a second source terminal; a reference voltage generation means for generating a reference voltage; a bias current generation means for generating a bias current determining an operating current; an error amplifier means for amplifying an error voltage for the reference voltage, the error amplifier means containing at least one phase compensation capacitor; a voltage-current output means for generating an output of a power circuit; and an output voltage-dividing means for detecting a fluctuation of the output voltage, wherein: a first input terminal of the error amplifier means is connected to the reference voltage generation means; a second input terminal of the error amplifier means is connected to the output voltage-dividing means; the error amplifier means comprises an input part consisting of a pair of the 1-type semiconductor elements and a load part consisting of a pair of the 2-type semiconductor elements; a noise suppression part consisting of a pair of the 1-type semiconductor elements is disposed between the input part and the load part; one terminal of the noise suppression part is connected to the first source terminal; a substrate terminal of the noise suppression part is connected to the second source terminal; and a pair of components of the noise suppression part is fabricated in different dimension to control the source voltage dependency of the output voltage.